

ABSTRACT OF THE DISCLOSURE

A plasma display panel includes a front substrate and a rear substrate, with a discharge space therebetween. Row and column electrodes extend on an inner surface of the front substrate. Each display line is defined by paired two adjacent row electrodes. A dielectric layer covers the row electrodes. Unit light-emission areas are formed in the discharge space at intersections of the row and column electrodes. A partition wall matrix comparts the unit light-emission areas from each other. A separation wall divides each unit light-emission area into a first discharge cell, in which discharge occurs across the paired two adjacent row electrodes associated with that unit light-emission area, and a second discharge cell, in which discharge occurs across one of the paired two adjacent row electrodes and the column electrode concerned. The first discharge cell communicates with the second discharge cell via a passage in each unit light-emission area.